

Fig.1

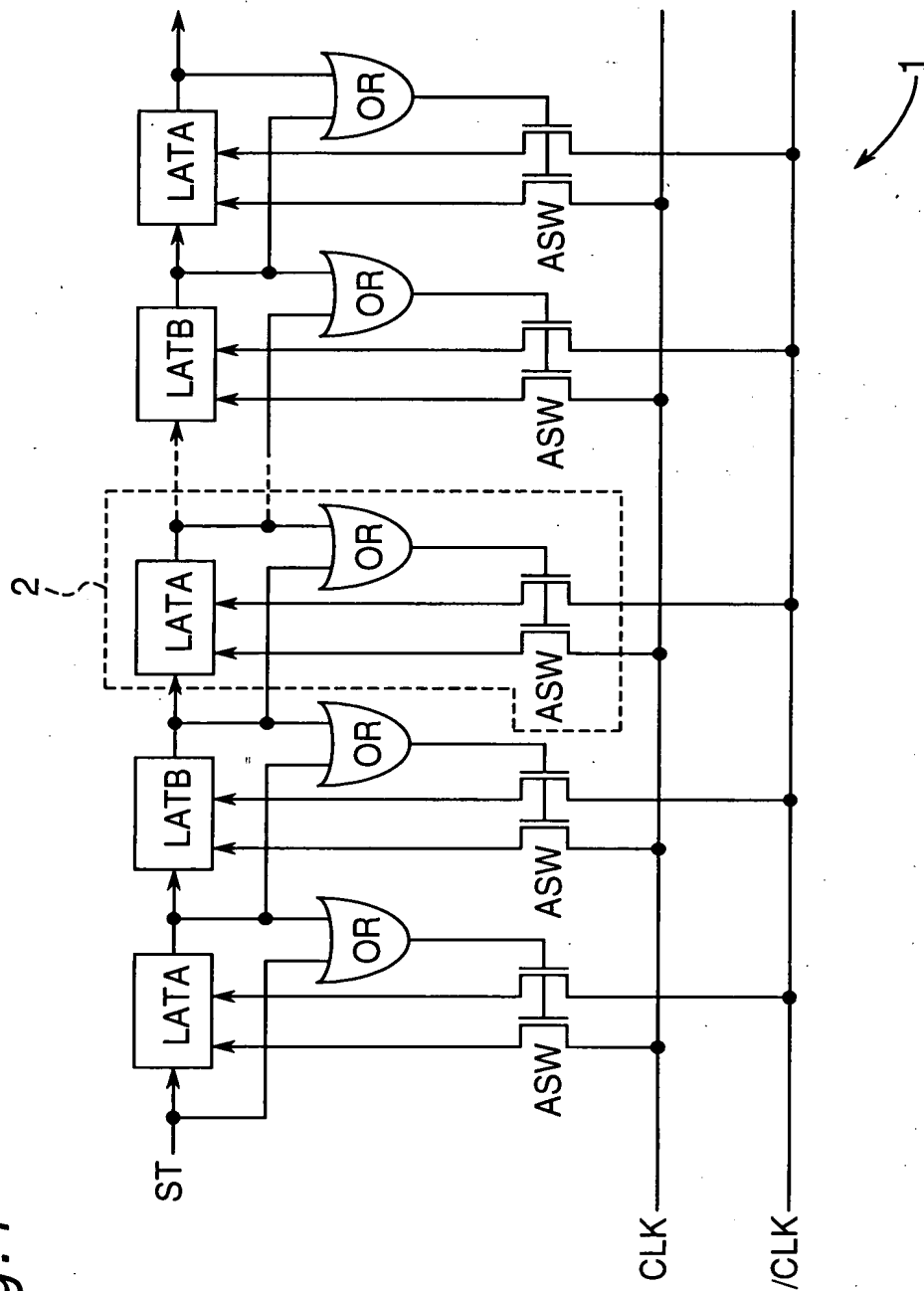


Fig.2A RELATED ART



Fig.2B RELATED ART



Fig.2C RELATED ART



Fig.3A RELATED ART



Fig.3B RELATED ART



Fig.3C RELATED ART



Fig. 4A



Fig. 4B

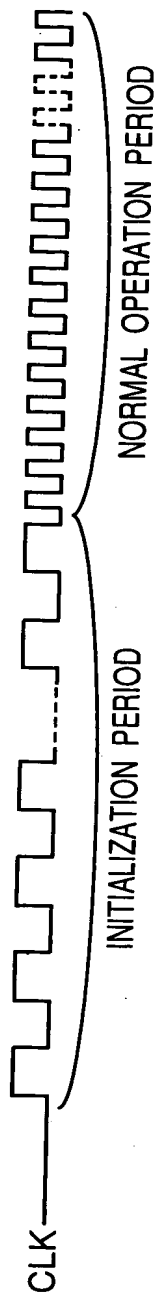


Fig. 5A



Fig. 5B



Fig. 5C



Fig. 5D

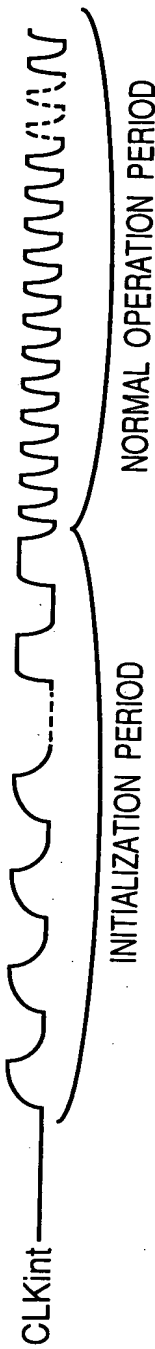


Fig. 6A

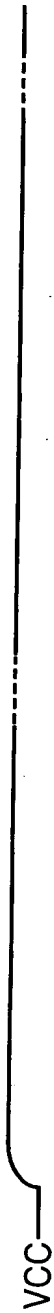


Fig. 6B

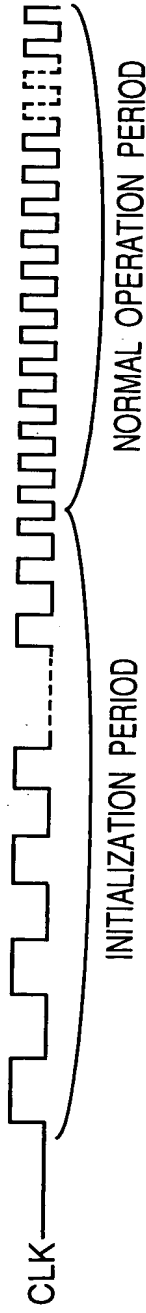


Fig. 7A



Fig. 7B



Fig. 7C

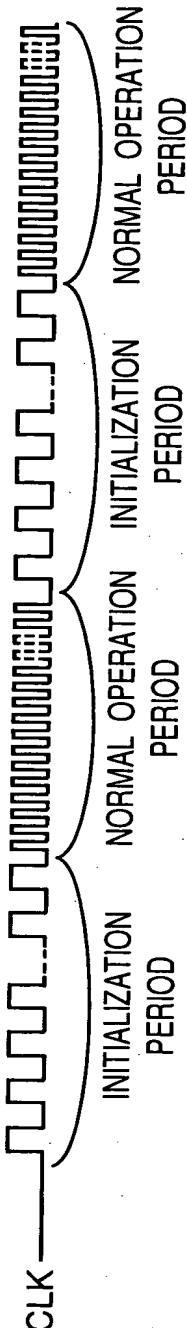


Fig.8

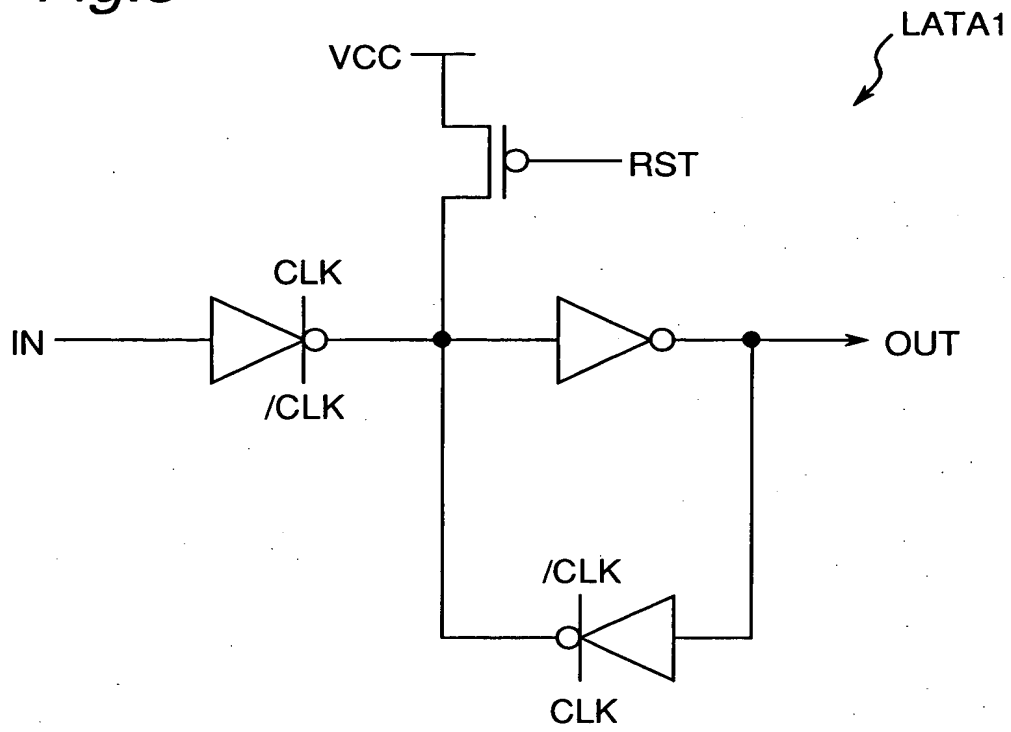
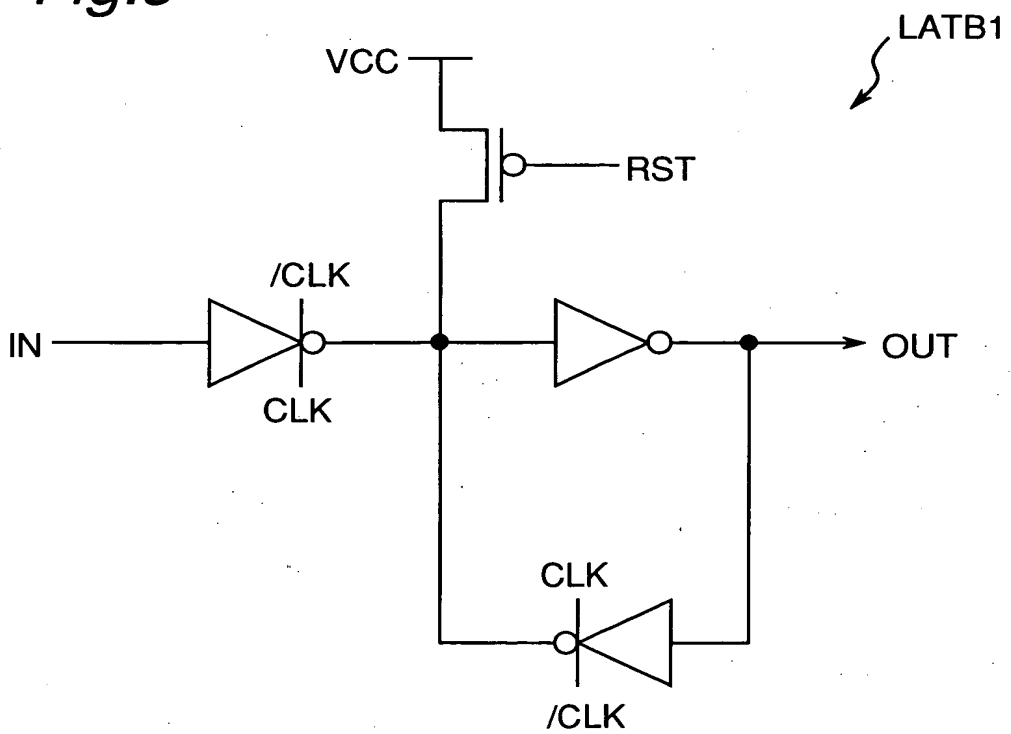


Fig.9



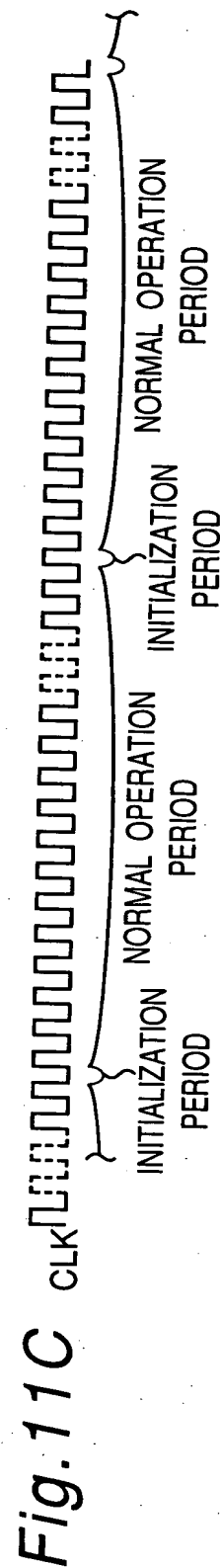
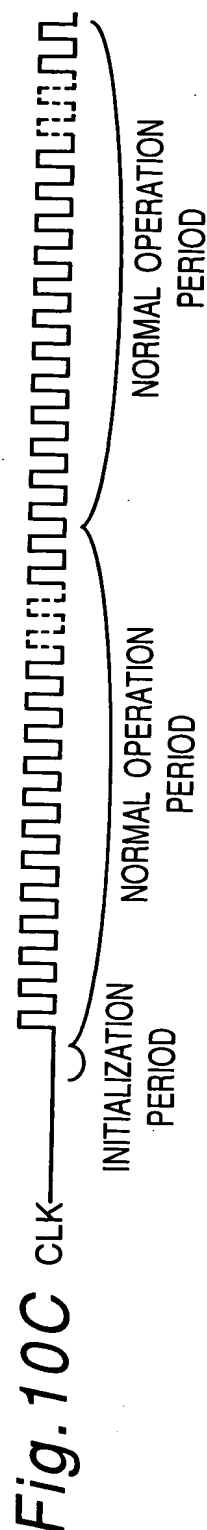


Fig.12

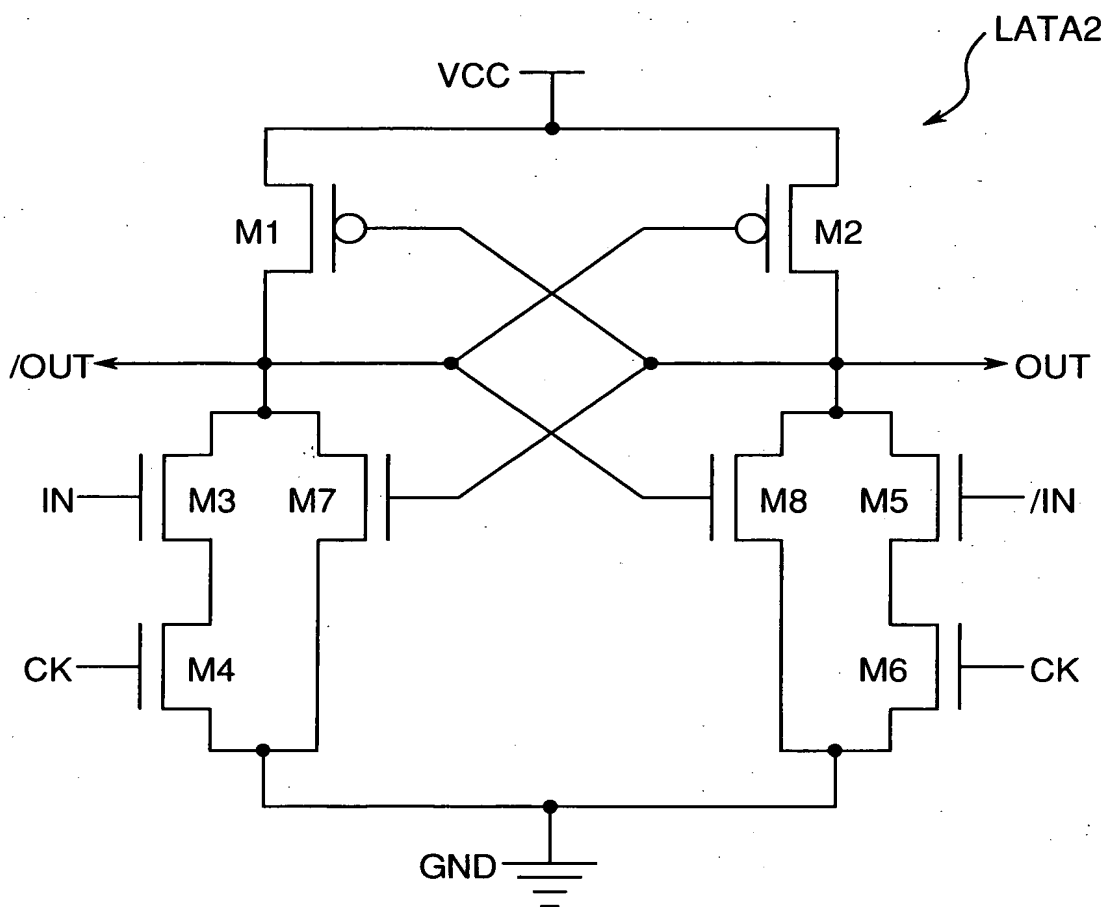


Fig.13

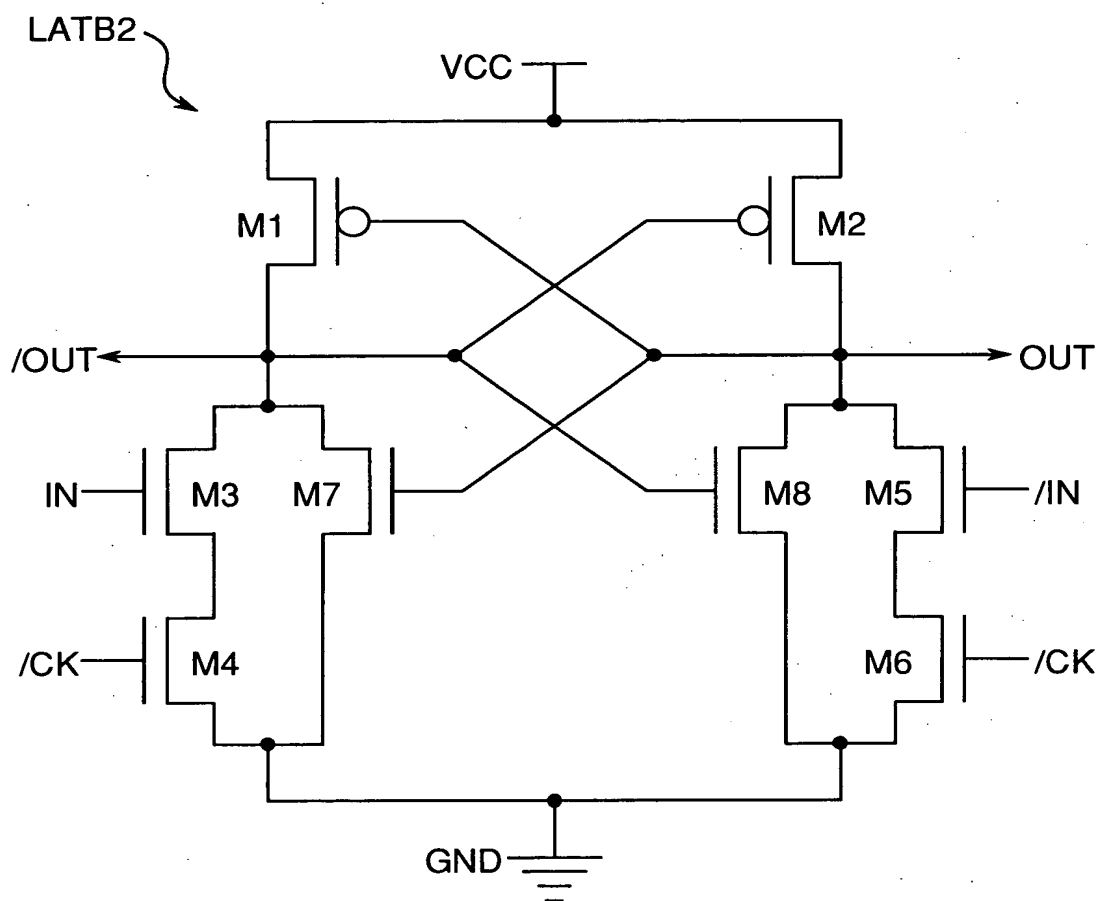


Fig. 14

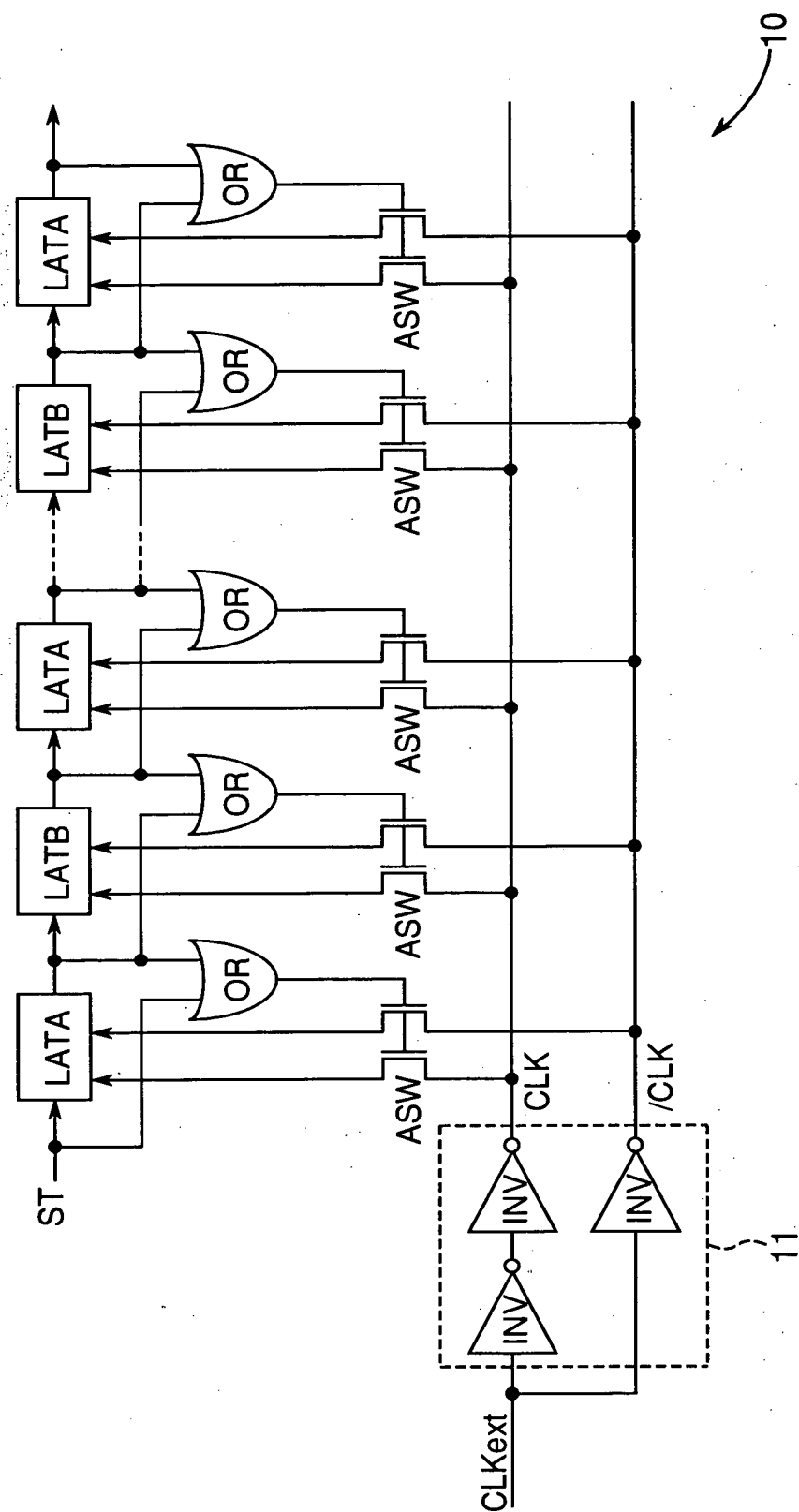


Fig. 15

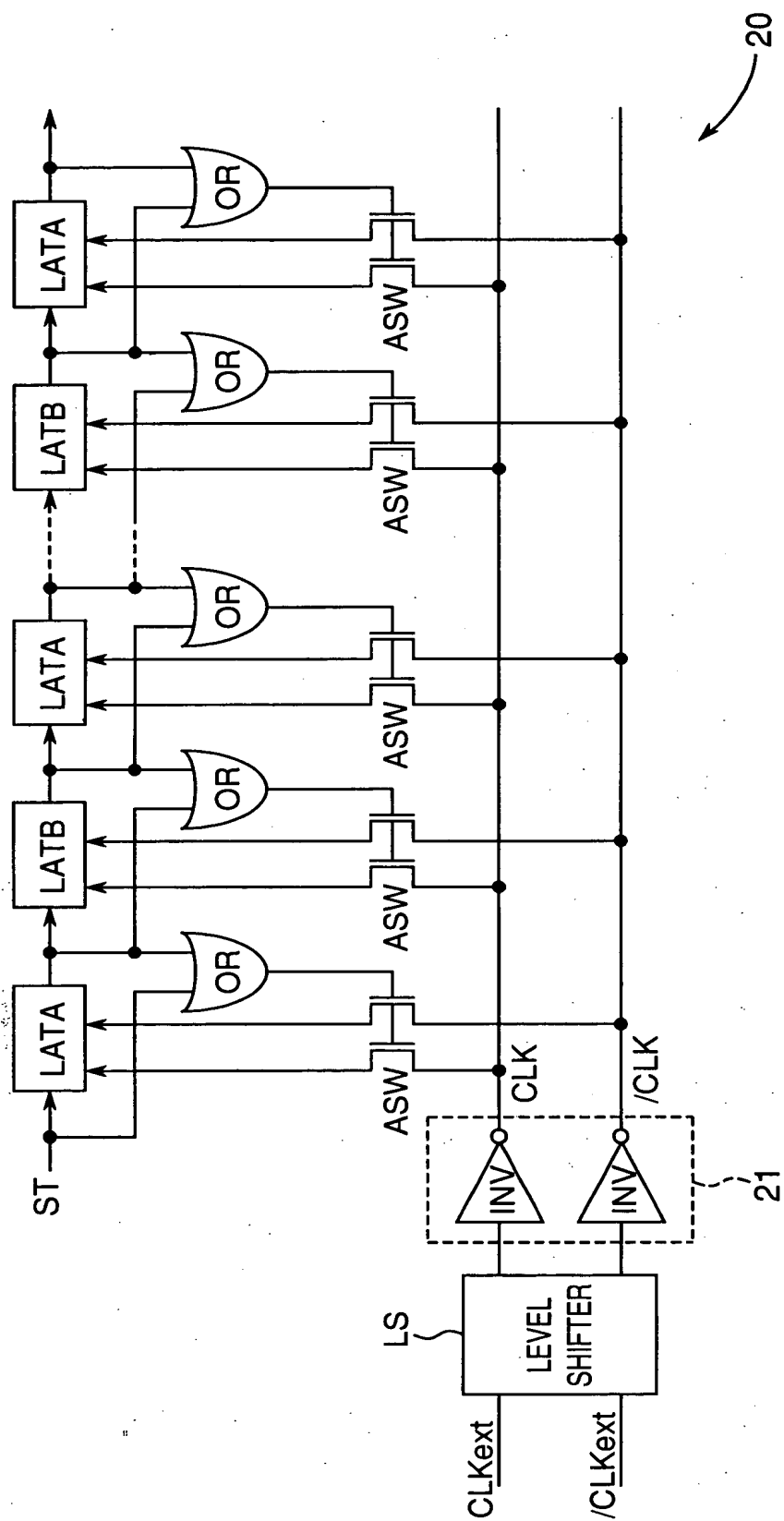


Fig. 16A

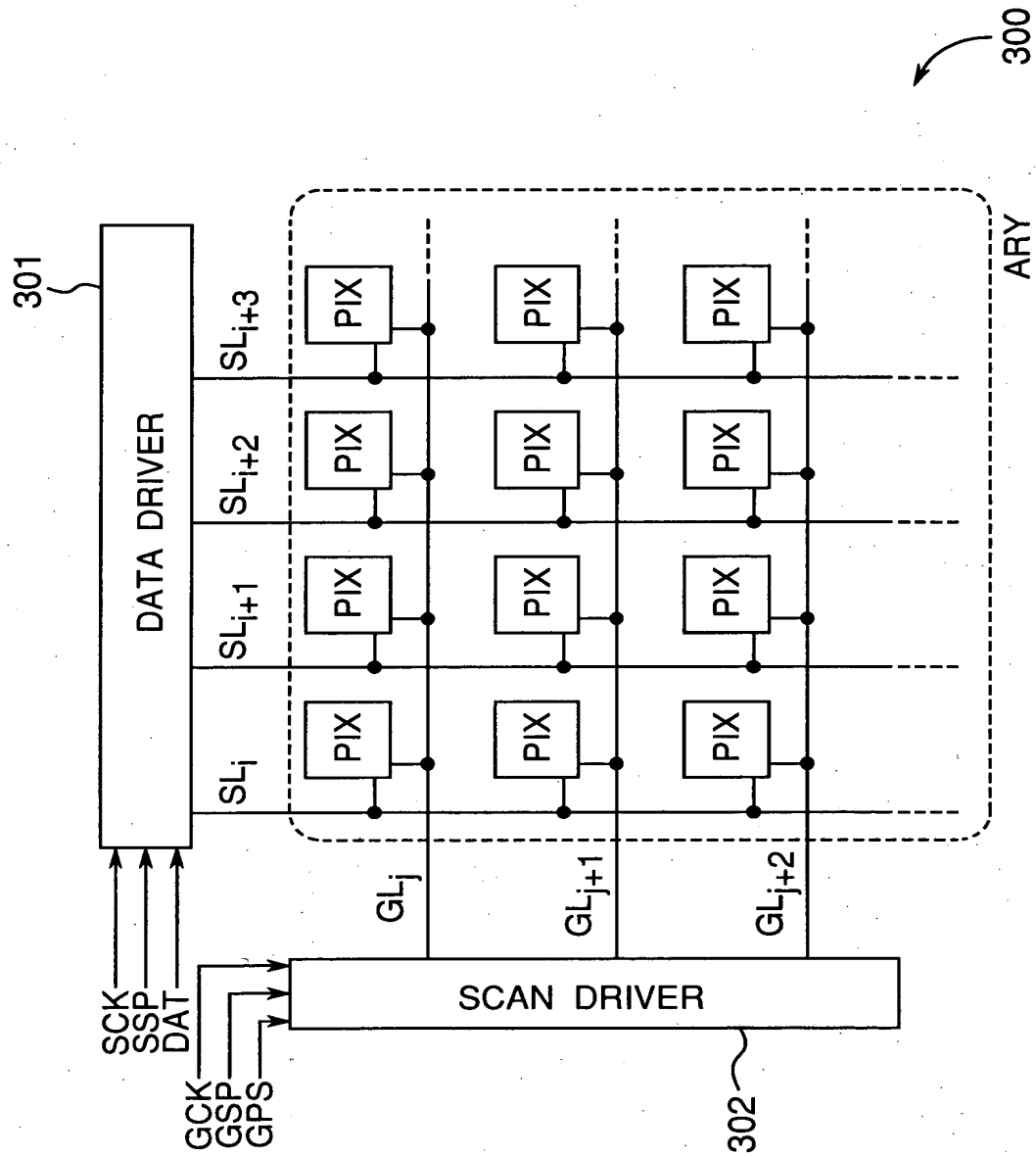
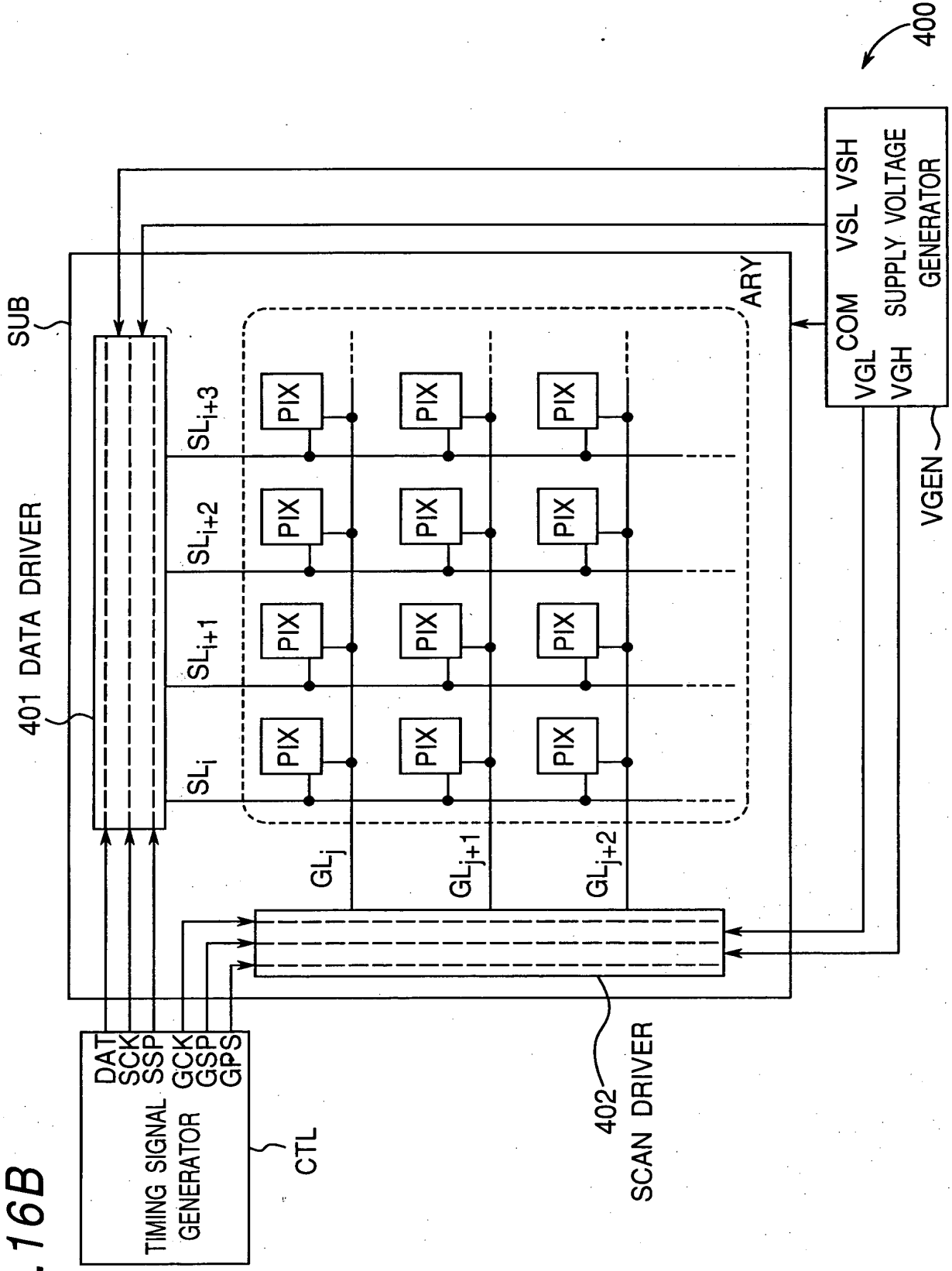


Fig. 16B



This cross-sectional view shows a central gate structure 38 on a substrate 31. The gate structure 38 is flanked by side regions 33 and 39. A thin layer 32 is located beneath the gate structure 38. A layer 34 is positioned below the side regions 33 and 39. A layer 35 is located beneath the central gate structure 38. A layer 36 is positioned below the side regions 33 and 39. A layer 37 is located beneath the central gate structure 38.

A perspective view of a rectangular box 31 with a lid 32.

A schematic diagram of a thin film structure. It shows a cross-section of a substrate with a thin layer on top. The thin layer is filled with diagonal hatching and is labeled 'a-Si' with a bracket.

Diagram illustrating a laser processing system. An **EXCIMER LASER** beam is directed onto a substrate (3). The substrate is shown as a layered structure, with the top layer being the target for processing.

A cross-sectional diagram of a device. It shows a substrate with a thin layer on top. Two rectangular regions on this layer are filled with diagonal hatching. A bracket above the right hatched region is labeled "ACTIVE REGION".

Fig. 18E

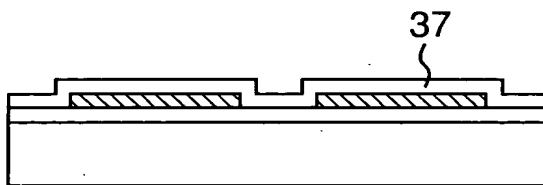


Fig. 18F

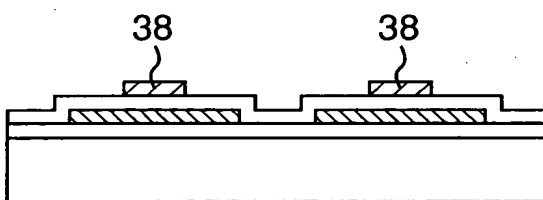


Fig. 18G

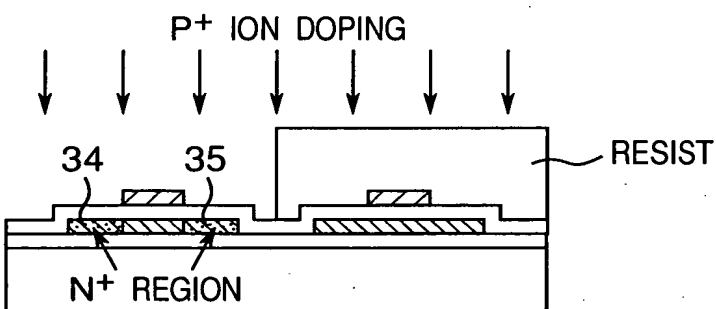


Fig. 18H

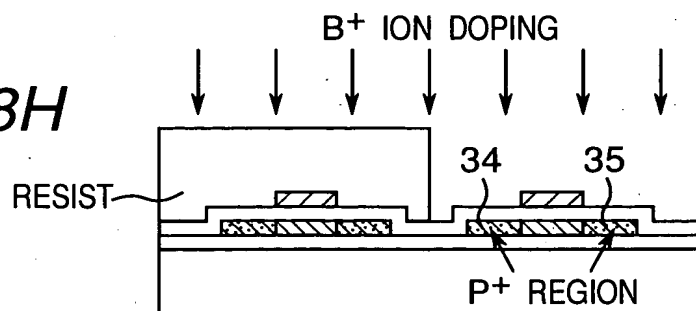


Fig.18I

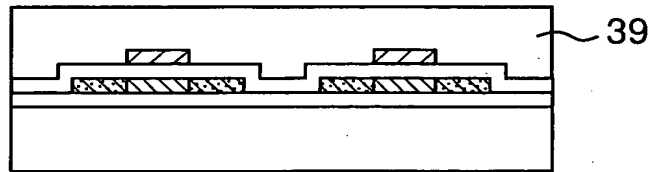


Fig.18J

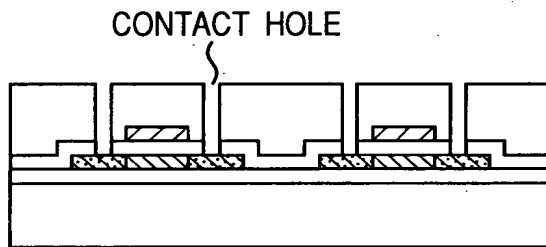


Fig.18K

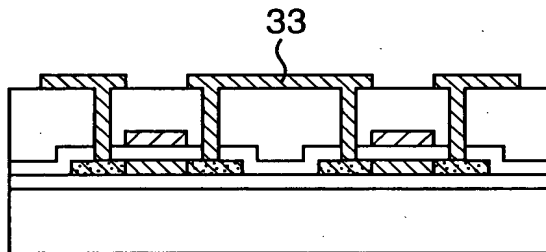


Fig.19 RELATED ART

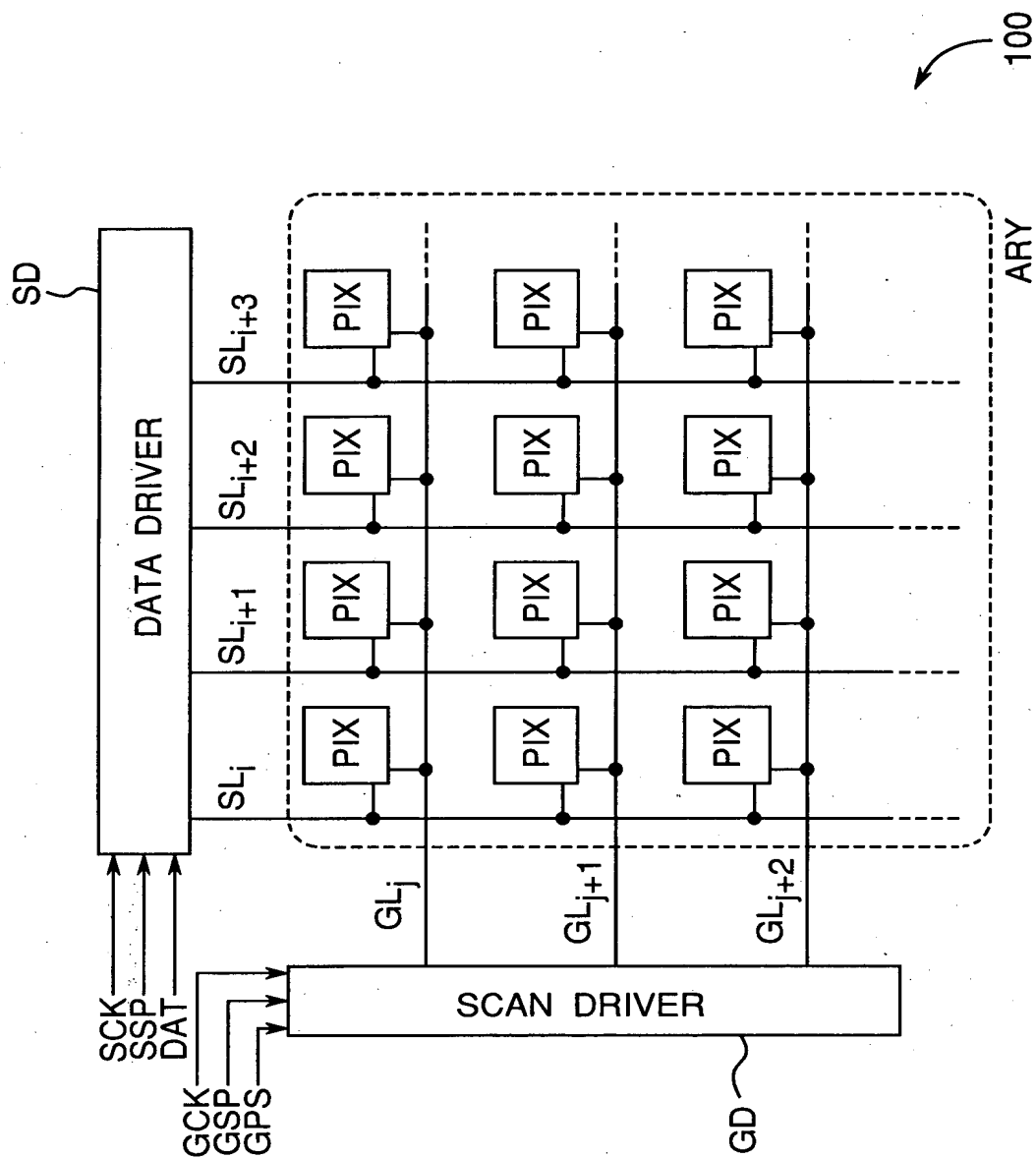


Fig.20 RELATED ART

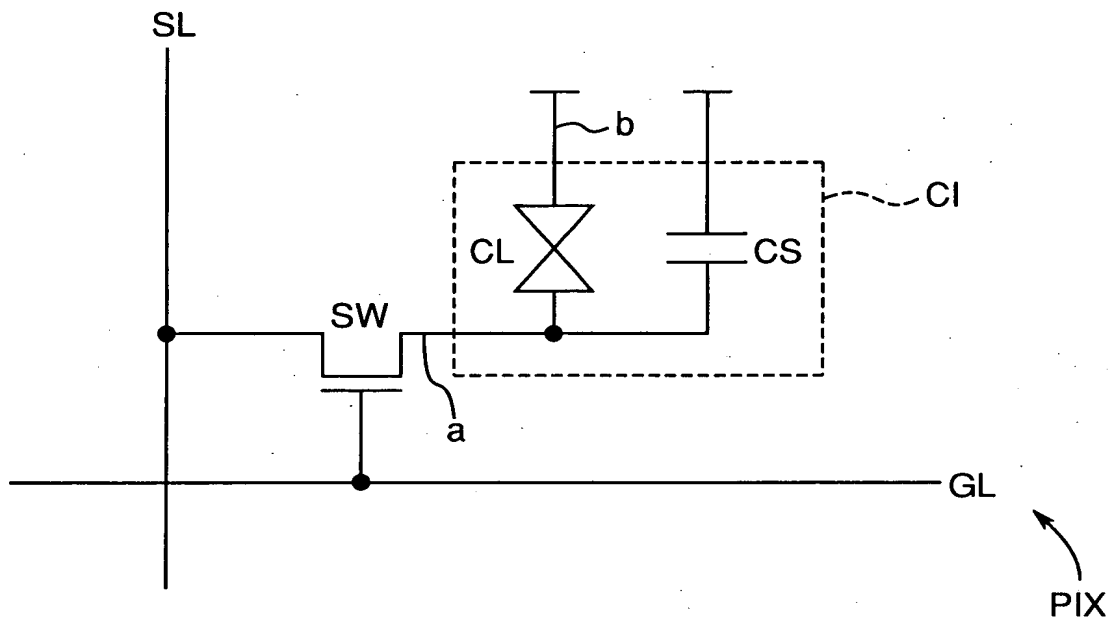


Fig.21 RELATED ART

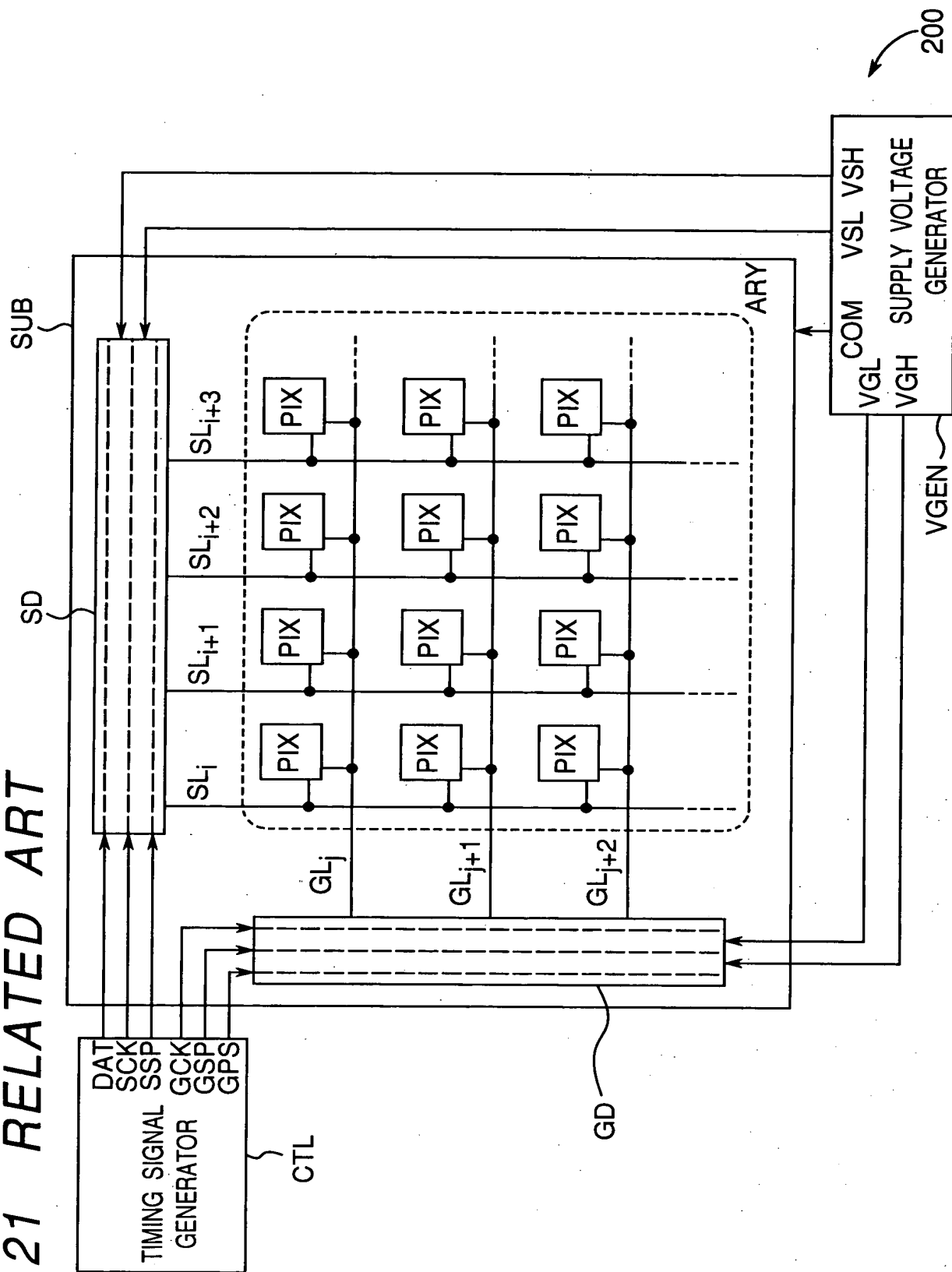


Fig.22 RELATED ART

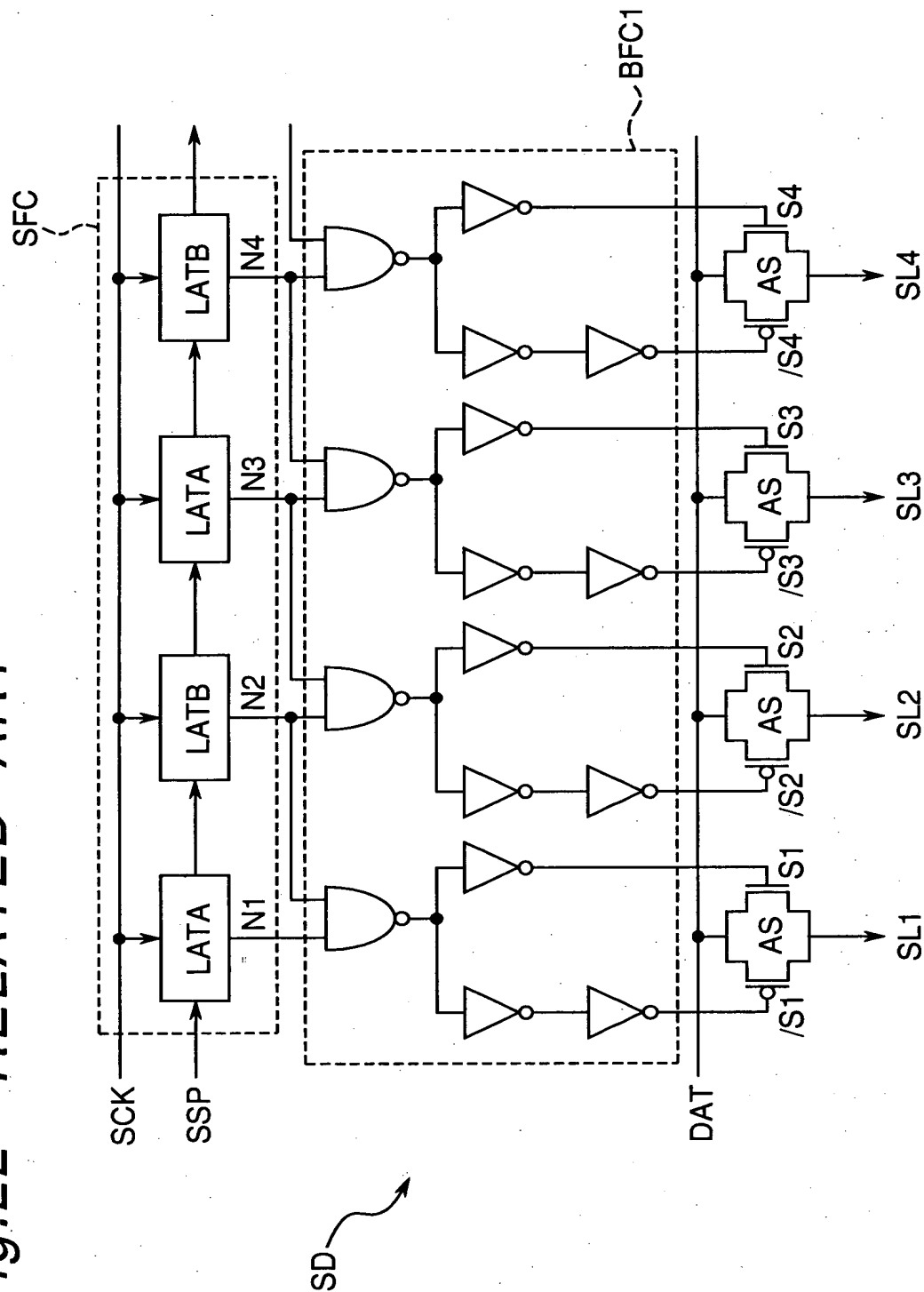


Fig.23 RELATED ART

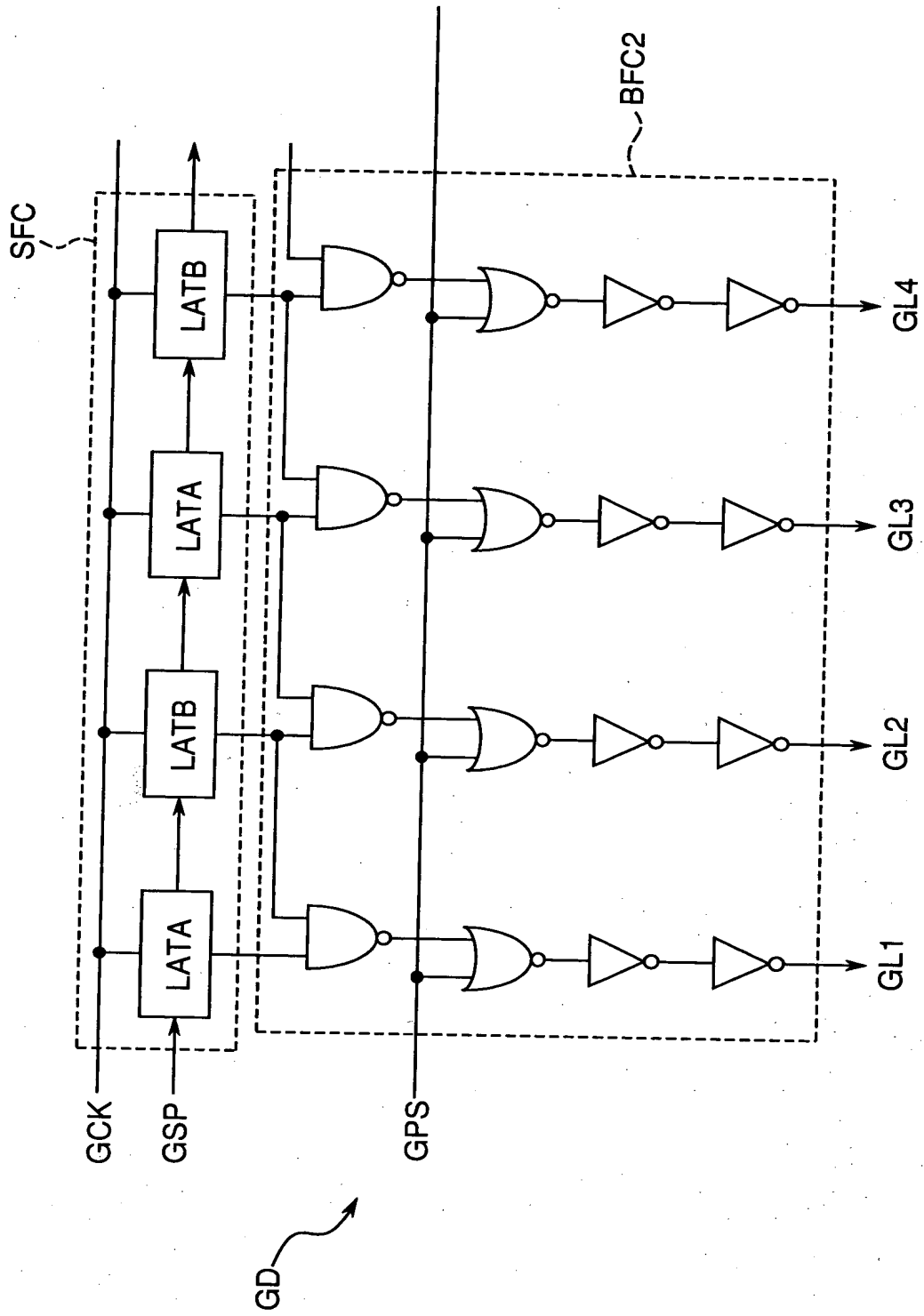


Fig.24 RELATED ART

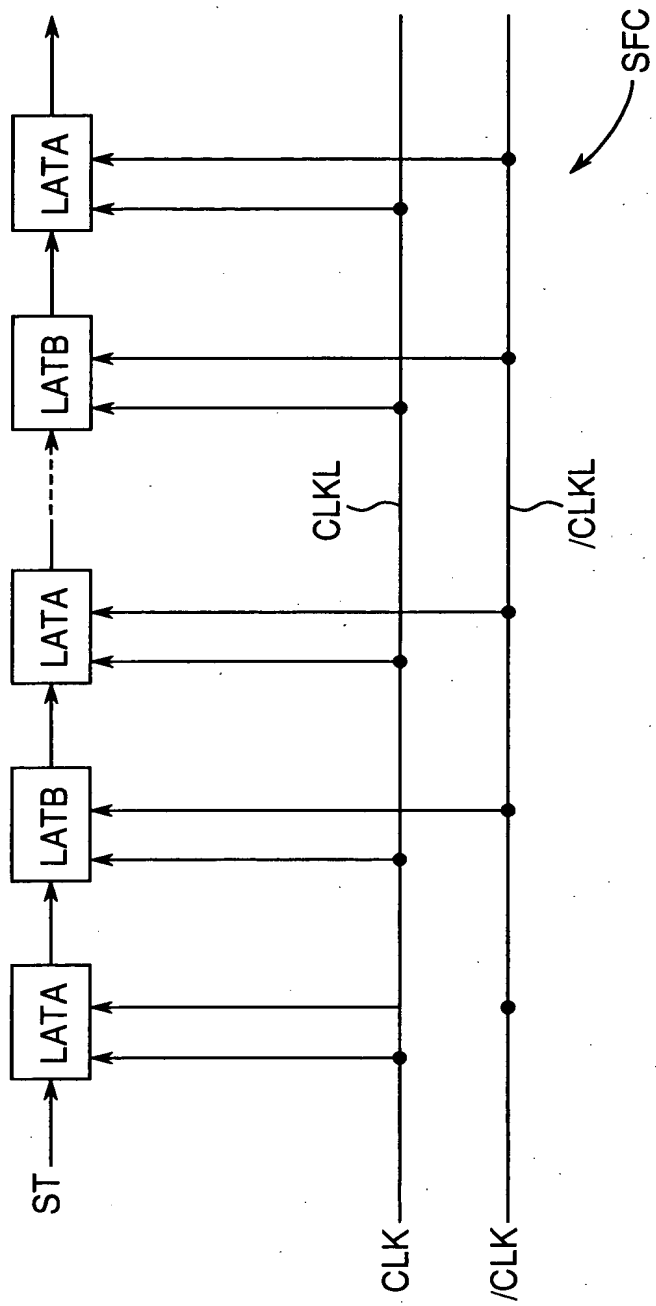


Fig.25A RELATED ART



Fig.25B RELATED ART

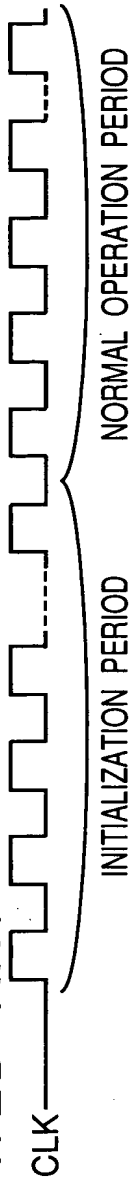


Fig.26 RELATED ART

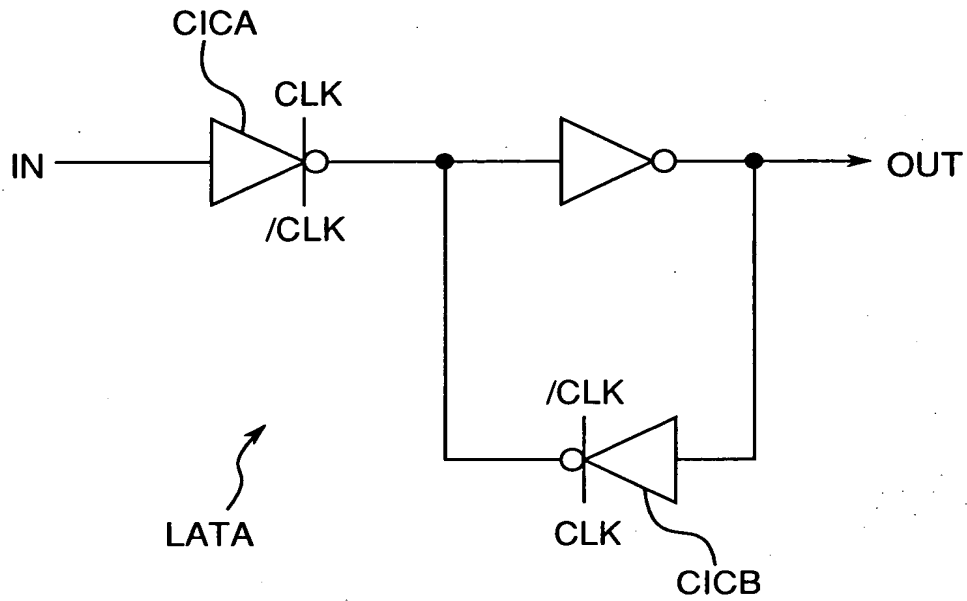


Fig.27 RELATED ART

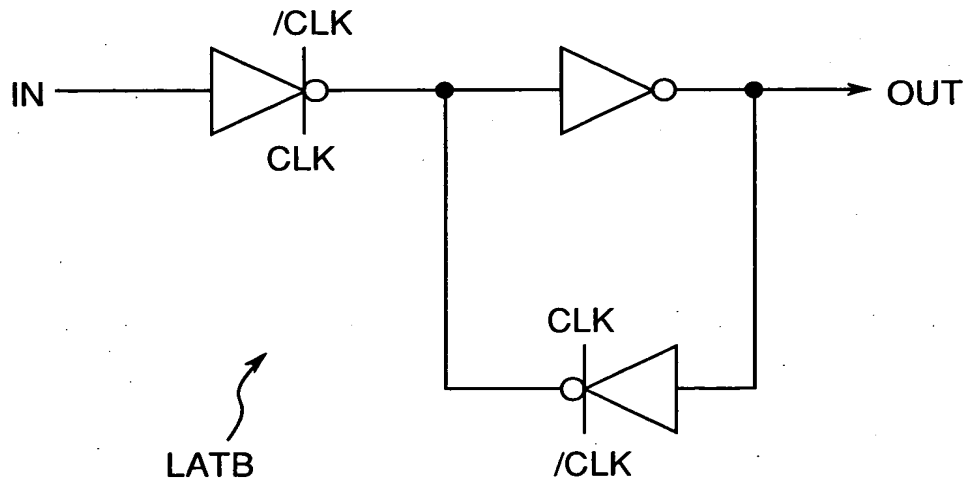


Fig.28 RELATED ART

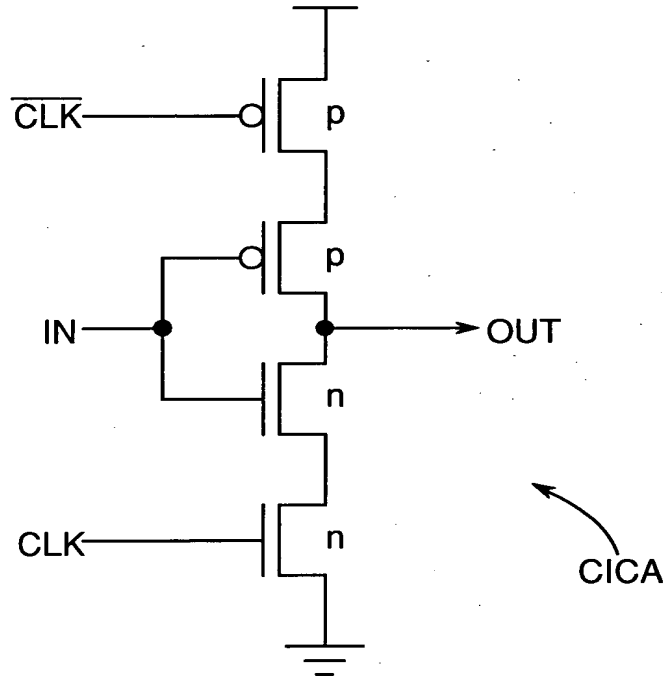


Fig.29 RELATED ART

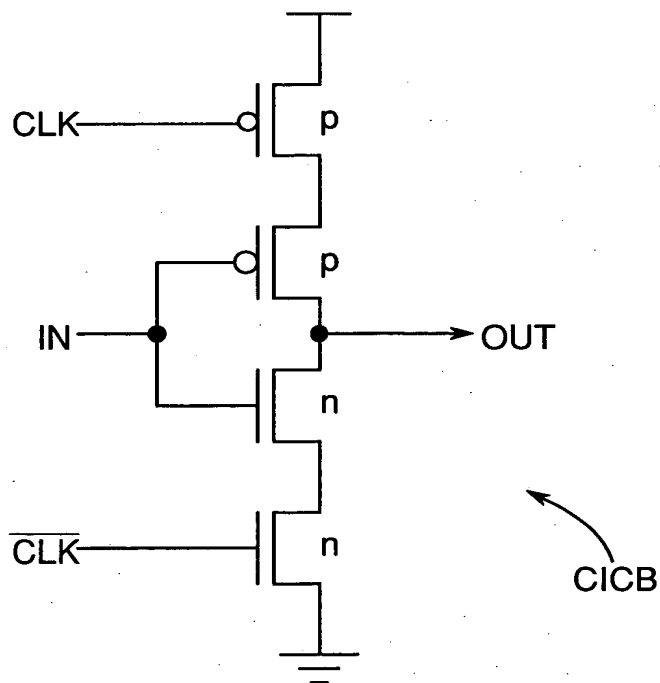


Fig. 30A

HSYNC



Fig. 30B

SCAN DRIVER START SG.



Fig. 30C

SCAN DRIVER CLOCK



Fig. 30D

DATA DRIVER START SG.



Fig. 30E

DATA DRIVER CLOCK

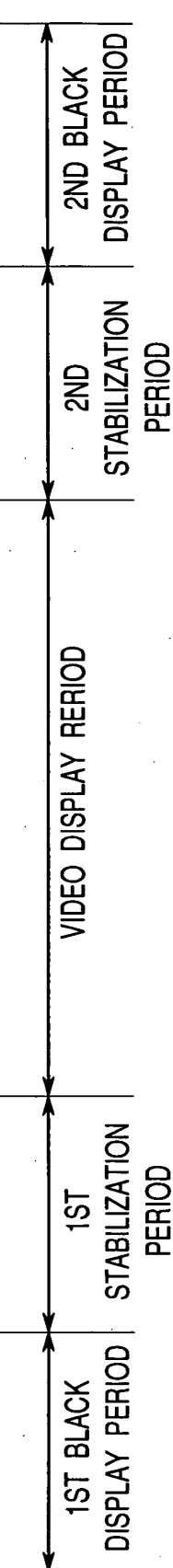


Fig. 31A

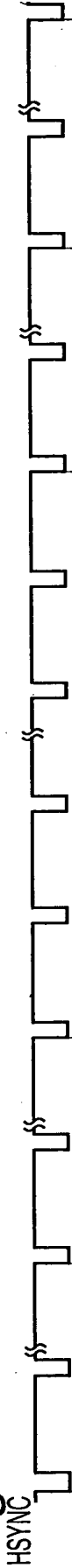


Fig. 31B

SCAN DRIVER START SG.



Fig. 31C

SCAN DRIVER CLOCK

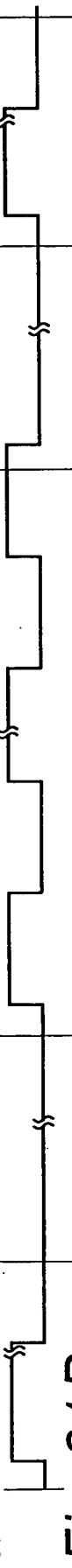


Fig. 31D

DATA DRIVER START SG.



Fig. 31E

DATA DRIVER CLOCK

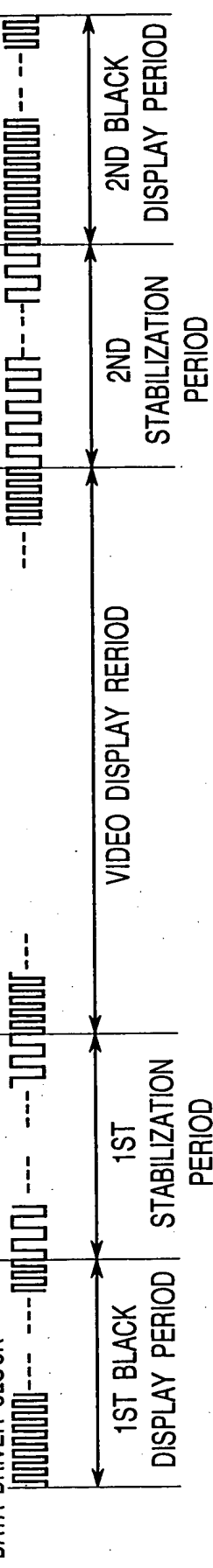


Fig.32 RELATED ART

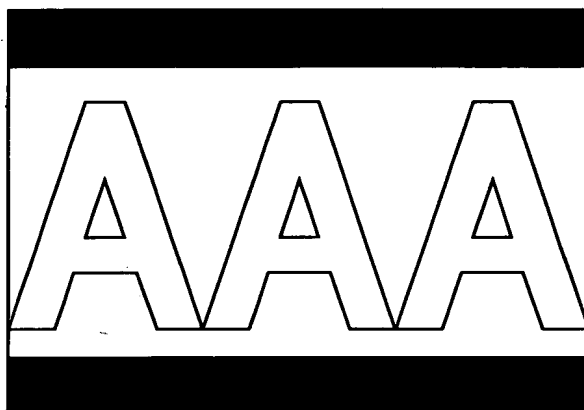


Fig.33A RELATED ART



Fig.33B RELATED ART

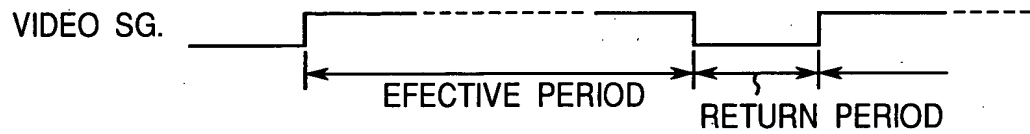


Fig.33C RELATED ART

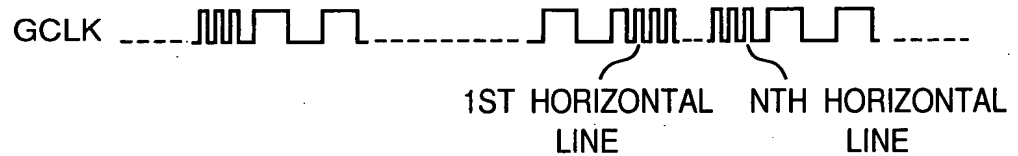


Fig.34 RELATED ART

